FIXED LENGTH MEMORY TO MEMORY INSTRUCTION SET; Inventors: David A. Fotland et al.; ocket No.: 20880-06031 Sheet 1 of 1

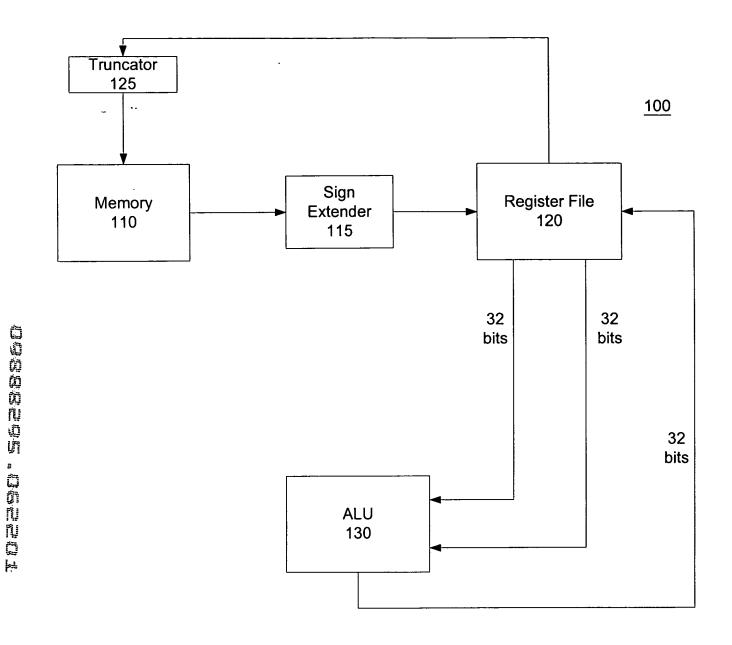


Figure 1A

FIXED LENGTH MEMORY TO MEMORY INSTRUCTION

SET; Inventors: David A. Fotiand et al.; Docket No.: 20880-06031 Sheet 2 of

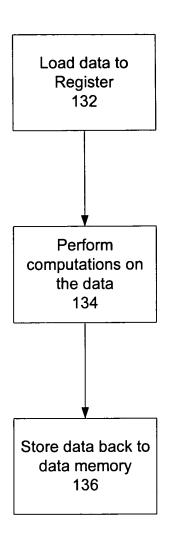


Figure 1B

FIXED LENGTH MEMORY TO MEMORY INSTRUCTION ET; Inventors: David A. Fotland et al.; cket No.: 20880-06031 Sheet 3 of 12

OPCode Register Regist 5 bits 5 bits	2 Target er Register 5 bits	Sub-OPCode 11 bits
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Figure 1C

OPCode Register 5 bits 5 bits	Immediate 16 bits
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Figure 1D

SET; Inventors: David A. Fotland et al.; bcket No.: 20880-06031 Sheet 4 of 1



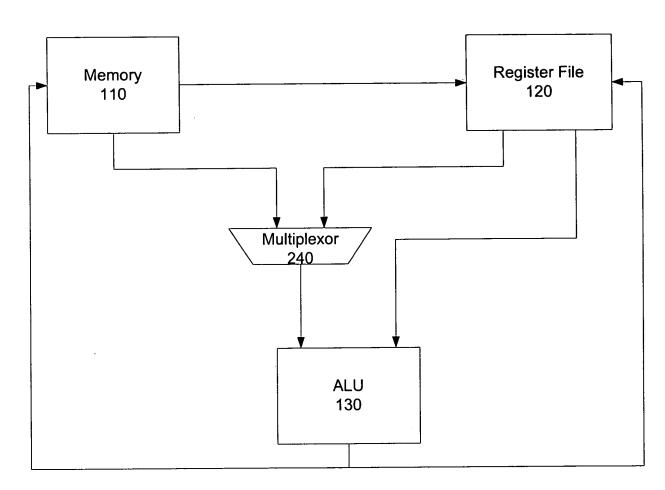


Figure 2

300

ET; Inventors: David A. Fotland et al.; bcket No.: 20880-06031 Sheet 5 of 1

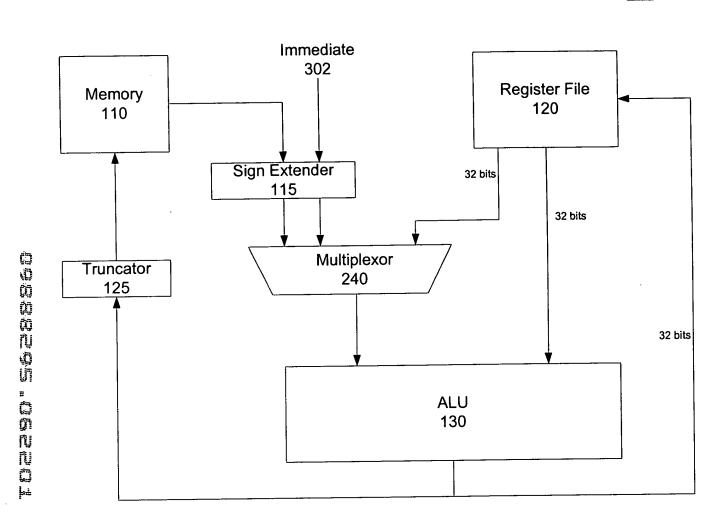
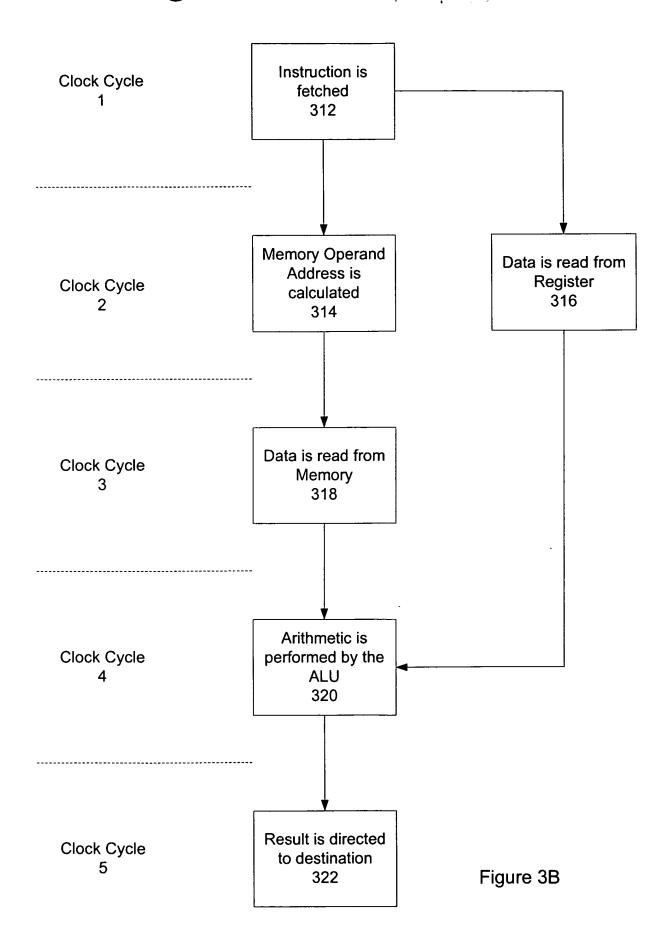


Figure 3A



SET; Inventors: David A. Fotland et al. ocket No.: 20880-06031 Sheet

Sheet 7 of 1

<u>400</u>

OPCode 5 bits	Destination 11 bits	Source2 Register 5 bits	Source1 11 bits
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Figure 4

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Docket No.: 20880-06031

SET; Inventors: David A. Fotland et al.;

Sheet 8 of

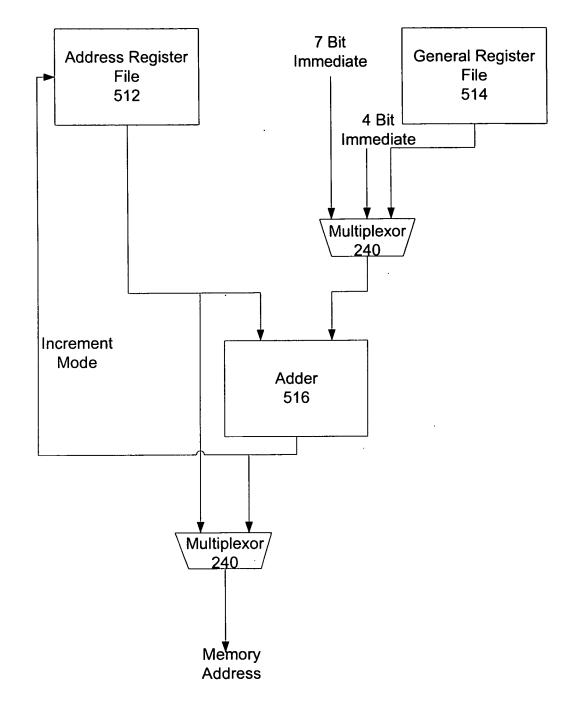


Figure 5

HONDOO HONDOODO

FIXED LENGTH MEMORY TO MEMORY INSTRUCTION

SET; Inventors: David A. Fotland et al.; Docket No.: 20880-06031 Sheet 9

1	ا ₆	l ₅		$ I_4 I_3 I_2 I_1 I_0 $
0	1	1	$A_2A_1A_0$	$R_4 R_3 R_2 R_1 R_0$
0	1	0		$ M _3 _2 _1 _0 $
0	0	1	Register Direct	
0	0	0	Immediate	

Row 1 Row 2 Row 3 Row 4

Row 5

Figure 6

FIXED LENGTH MEMORY TO MEMORY INSTRUCTION

SET; Inventors: David A. Fotland et al.; Docket No.: 20880-06031 Sheet 10

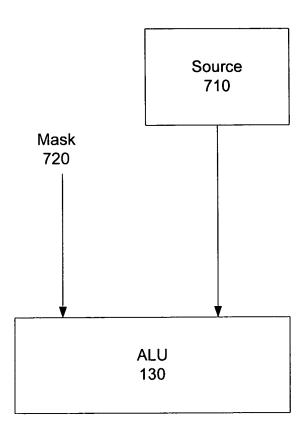


Figure 7

Docket No.: 20880-06031

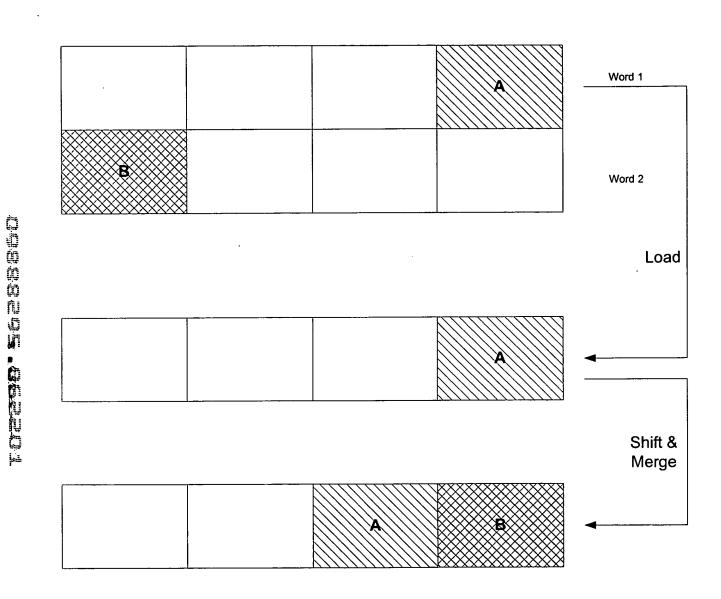
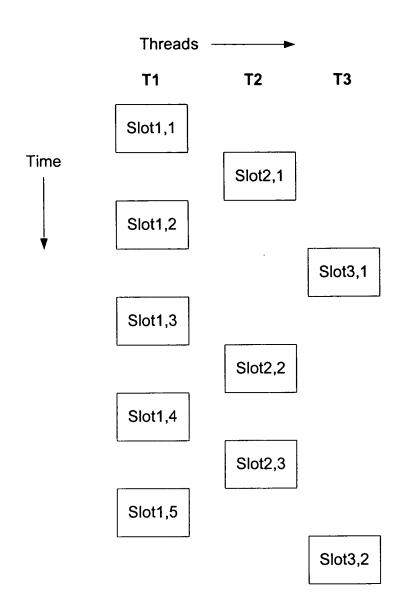


Figure 8

Sheet 12



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Figure 9